

AMENDMENTS TO THE SPECIFICATION

Please amend the specification as follows:

On page 1, above paragraph 1, please add the section heading: Field of the Invention.

On page 1, above paragraph 2, please add the section heading: Background of the Invention.

On page 2, above paragraph 1, please add the section heading: Summary of the Invention.

On page 4, above the second full paragraph, please add the section heading: Brief Description of the Drawings.

Above the paragraph bridging pages 4 and 5, please add the section heading: Detailed Description.

Please replace the paragraph bridging pages 7 and 8 with the following paragraph:

Figure 5 schematically illustrates the circuit diagram for the logic circuit 50. The logic circuit comprises a comparator 90 having an inverting input connected to the output of the peak rectifier 30 and a non-inverting input connected to receive voltage threshold TH_{V1} . An output of the comparator 90 is supplied to a first input of an AND gate 94. A second comparator 92 has an inverting input connected to the generator output current measuring element 52 and a non-inverting input connected to receive the first current threshold TH_{I1} . An output of the comparator 92 is connected to a second input of the AND gate 94. An output of the AND gate 94 is connected to a first input of an electronically operated switch 96. The switch 96 is controlled such that following generator start up it supplies the output of the AND gate 94 to the latch 32. However, during a generator start up phase the switch 96 is controlled so as to disconnect the output of the AND gate 94 from the latch 32. The switch 96 is controlled by a timer 98 which has a first input responsive to a generator enable signal used to switch the generator on and the second input responsive to an output of a further comparator 100 which has its is non-inverting input connected to receive the rectifier output voltage V_{peak} and its inverting input connected to receive a second voltage reference TH_{V2} . Thus the comparator 100 performs the comparison of step 78.